

	Type	Hits	Search Text
1	BRS	15761	gate adj array
2	BRS	4058	(gate adj array) and (transistors)
3	BRS	1234	(gate adj array) and (second with (transistors))
4	BRS	529	(gate adj array) and (second with (transistors)) and ((PMOS or P\$1type)) and ((NMOS or N\$1type))
5	BRS	3733	(gate adj array).ti.
6	BRS	187	((gate adj array) and (transistors)).ti.
7	BRS	1	((gate adj array) and (transistors) and (different adj (sizes or size))).ti.
8	BRS	47	((gate adj array) and (transistors) with (different adj (sizes or size)))
9	BRS	10	((gate adj array) with (transistors) with (different adj (sizes or size)))
10	BRS	2106	clock adj buffer
11	BRS	15	(clock adj buffer) with (MOS adj transistor)
12	BRS	0	((gate adj array) and (transistors)).ti.
13	BRS	631	(gate adj array).ti.

	DBs	Time Stamp	Comments	Error Definition
1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 14:44		
2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 14:45		
3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 14:47		
4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 14:50		
5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 16:31		
6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 16:30		
7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 15:22		
8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 15:23		
9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 16:25		
10	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 16:26		
11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/10 16:27		
12	JPO	2002/07/10 16:31		
13	JPO	2002/07/10 16:31		

Errors	
1	0
2	0
3	0
4	0
5	0
6	0
7	0
8	0
9	0
10	0
11	0
12	0
13	0